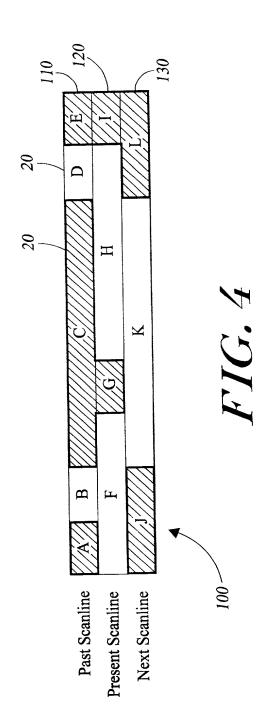
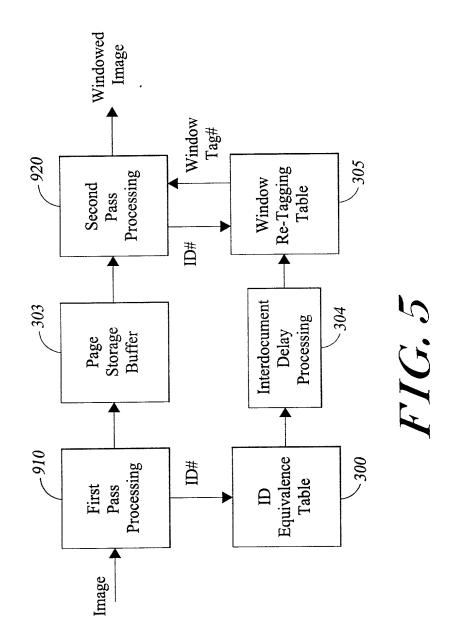


FIG. 3





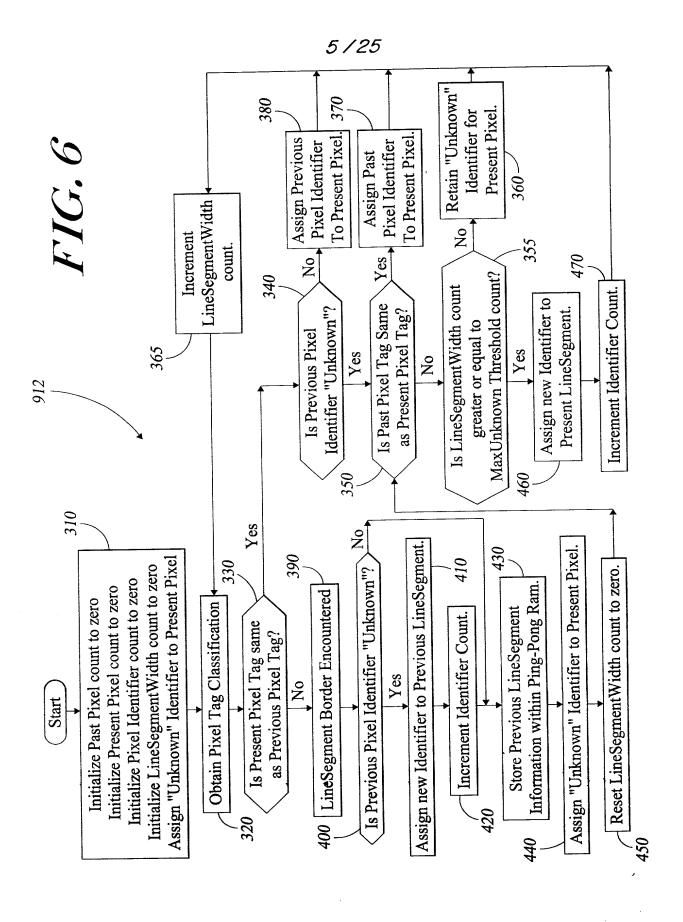




FIG. 7A

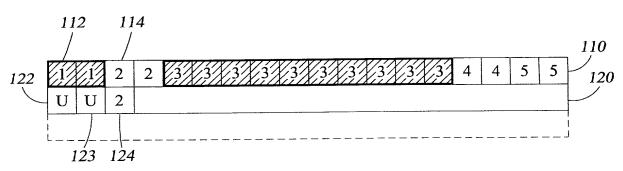


FIG. 7B

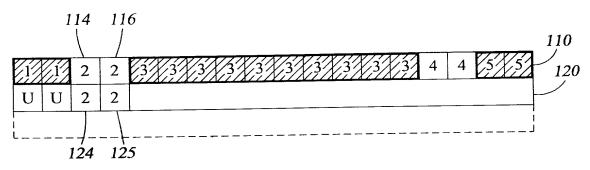


FIG. 7C

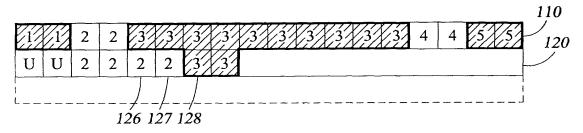


FIG. 7D

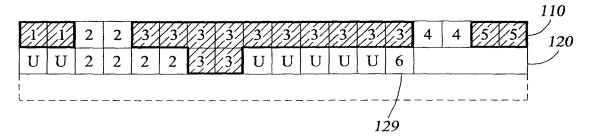


FIG. 7E

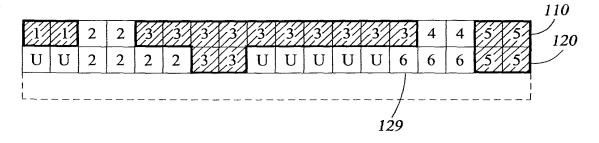
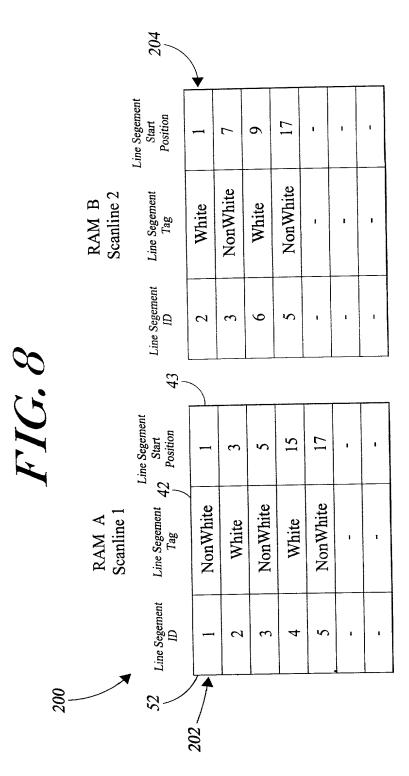
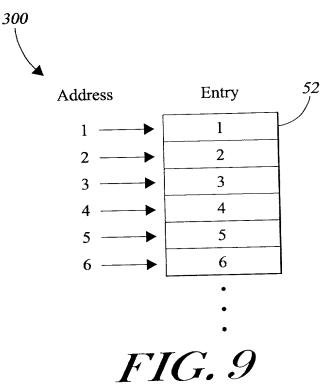
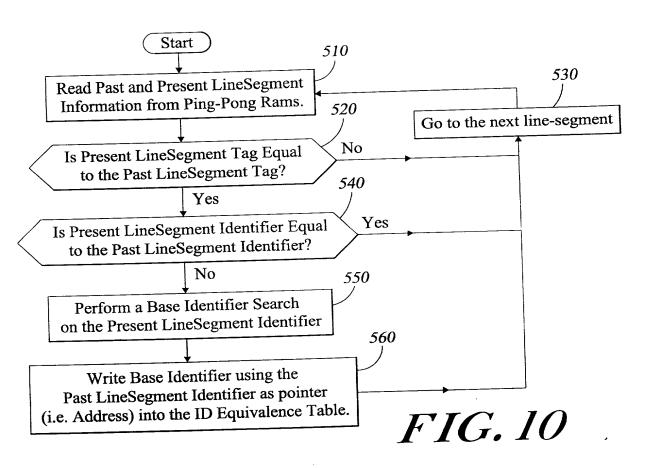


FIG. 7F







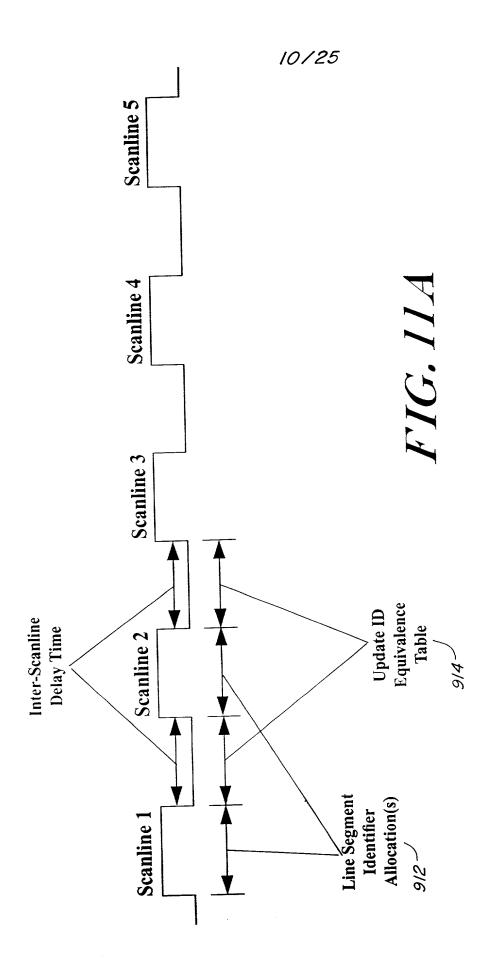
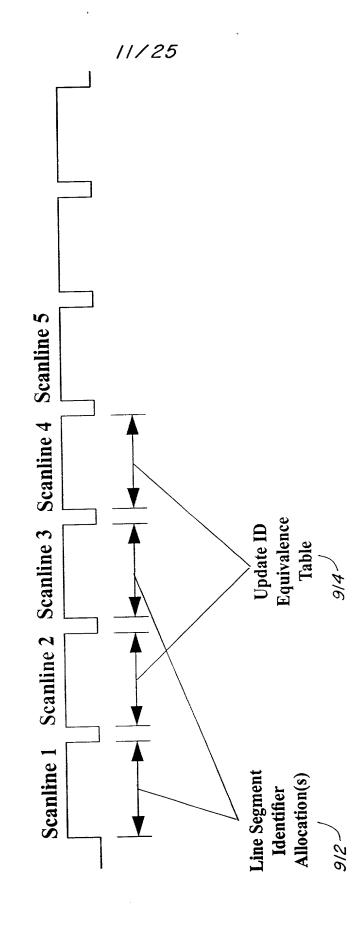


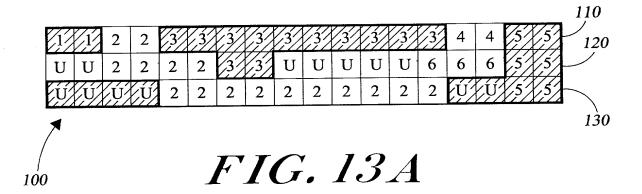
FIG. 11B

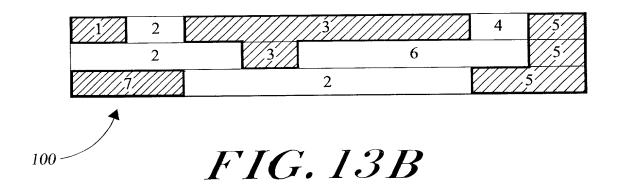


300

Address	Entry		
1	1		
2	2		
3	3		
4	6		
5	5		
6	6		
7	7		
-	-		

FIG. 12





RAM B Scanline 2

RAM A Scanline 3

14/25 *←216* ←204 ←208 ←212 Line Segment Start Position 17 6 Line Segment Tag Non White Non White White White Line Segment ID 9 (1 3 Line Segment Start Position 15 S Line Segment Tag Non White Non White White Line Segment ID α S 210-> 206→

FIG. 14

300 —

Address	Entry
1	1
2	2
3	3
4	6
5	5
6	2
7	7
-	-

FIG. 15

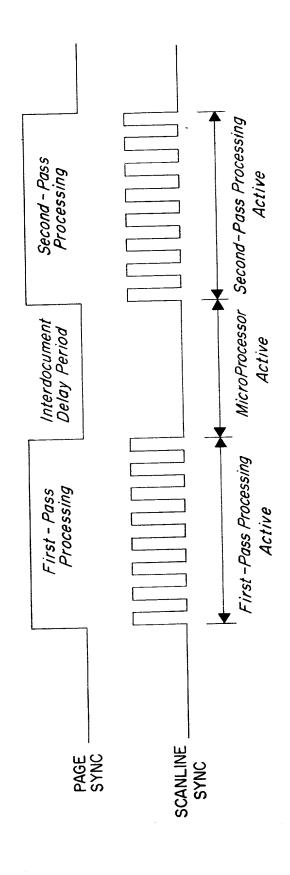
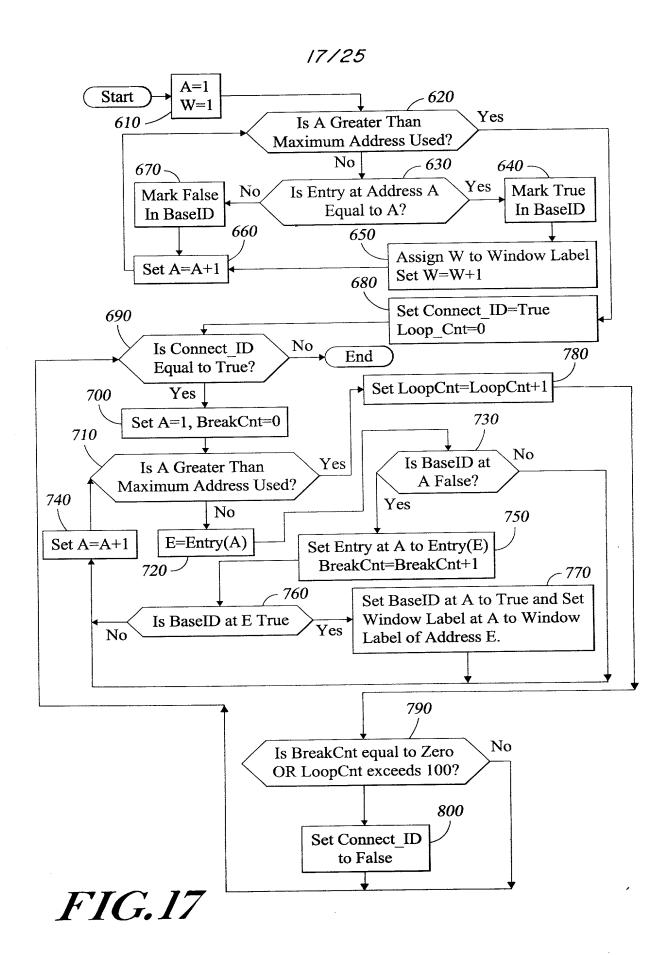


FIG. 16



		307 308		309	
	Address	Entry	BaseID	Window	
	1	1	True	W1	
	2	2	True	W2	
	3	3	True	W3	
	4	6	False		
302	5	5	True	W4	
	6	2	False		
	7	7	True	W5	
	-	-	-	-	

FIG. 18A

		_30	07	308 309
	Address	Entry	BaseID	Window
	1	1	True	W1
	2	2	True	W2
	3	3	True	W3
	4	2	False	
302	5	5	True	W4
	6	2	True	W2
	7	7	True	W5
	-	-	-	-

FIG. 18B

		_3	07 _30)8
	Address	Entry	BaseID	Window
	1	1	True	W1
	2	2	True	W2
	3	3	True	W3
	4	2	True	W2
302	5	5	True	W4
	6	2	True	W2
	7	7	True	W5
	-	-	-	-

FIG. 18C

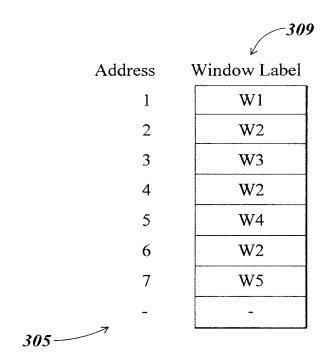
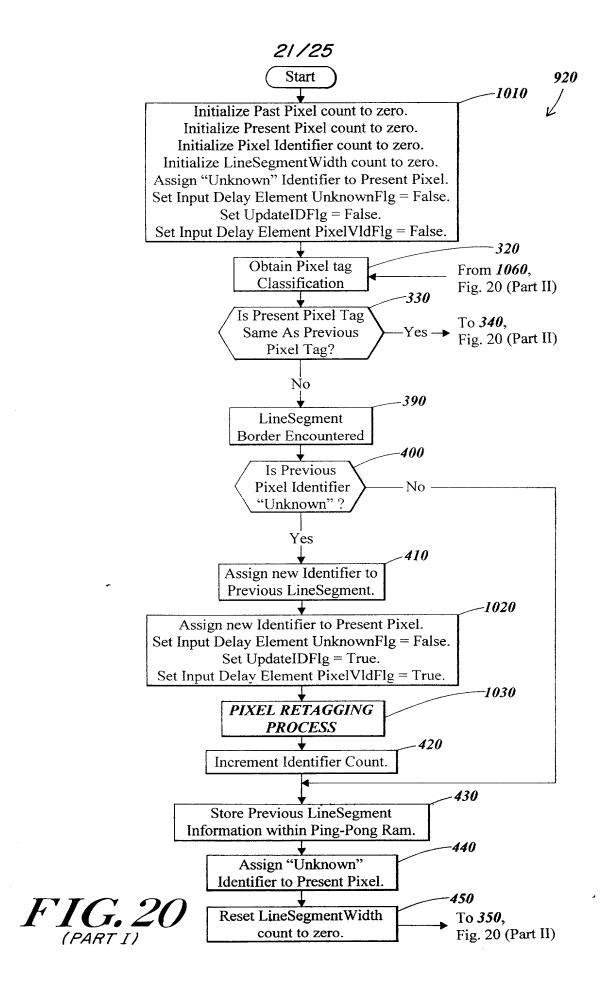
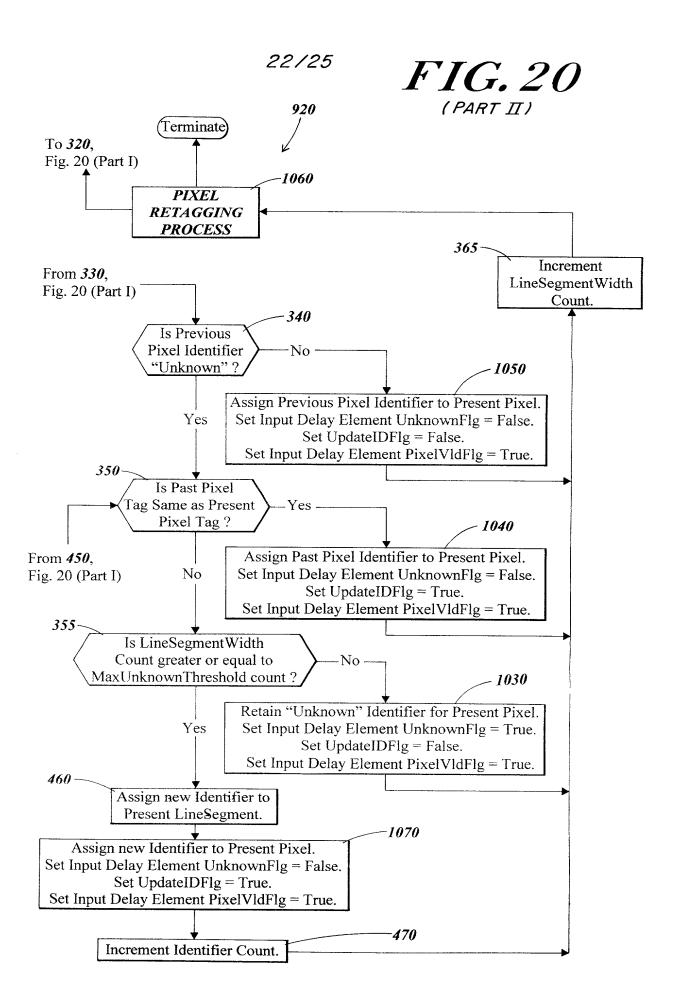
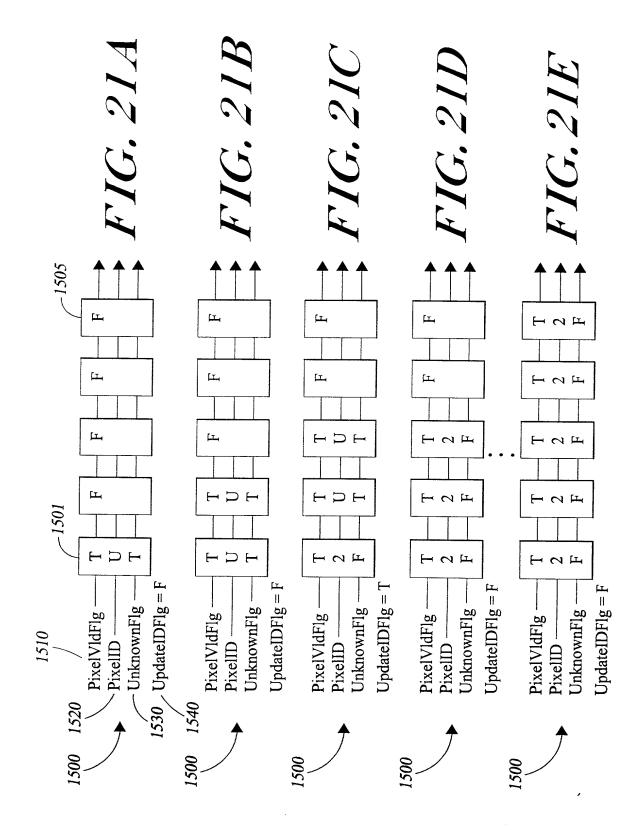


FIG. 19







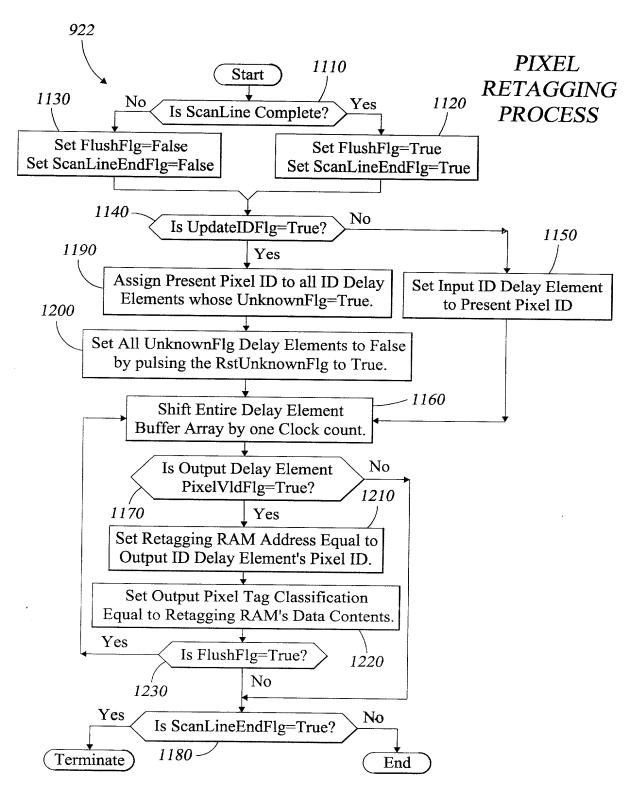
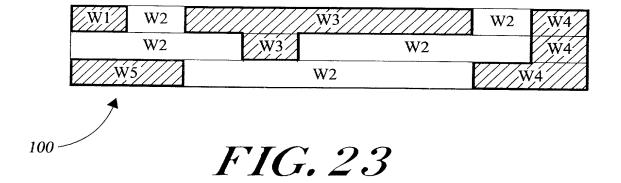


FIG. 22



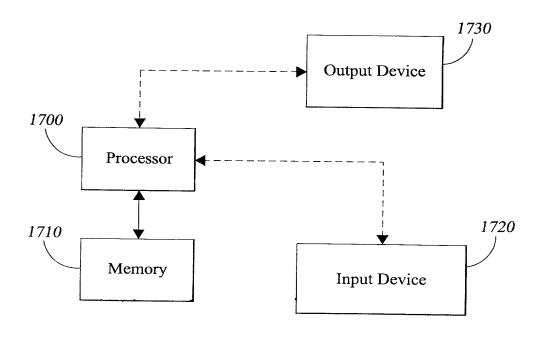


FIG. 24